

**Amendments to the Claims:**

Claims 14-16 are pending in this application. This listing of claims will replace all prior versions, and listings of claims in the application:

**Listing of Claims:**

1-13 (Cancelled)

14. (Currently amended) An integrated circuit, comprising:

~~a)~~ a standard cell array comprising rows of cells having a row pitch and spacing between cells in at least one row of said standard cell array; and

~~b)~~ a MACRO, embedded within the standard cell array, comprising rows of cells having pitch substantially equal to the row pitch.

15. (Currently amended) An integrated circuit, comprising:

~~a)~~ a standard cell array region, comprising rows of cells having a row pitch;

~~b)~~ ~~a pair of power conductors extending across each row;~~ and

~~c)~~ a MACRO region embedded within said standard cell array region, comprising rows of cells having a pitch equal to said row pitch;

a pair of power conductors extending across cells of said standard cell array and cells of said MACRO region.

16. (Currently amended) An integrated circuit, comprising:

~~a)~~ cells of a standard cell array;

~~b)~~ cells of a MACRO, embedded in the standard cell array, having a row pitch substantially the same as the standard cell array and

~~e)~~ parallel power conductors running adjacent all of said cells and running in the same level of said integrated circuit.